

SYSTEM AND METHOD FOR DETECTING QUIESCENT
CURRENT IN AN INTEGRATED CIRCUIT

5 ABSTRACT OF THE DISCLOSURE

10 A method for detecting quiescent current in an
integrated circuit is provided that includes detecting a
magnetic field generated by the quiescent current and in
response generating a magnetic field signal that is
indicative of the detected magnetic field. The magnetic
field signal is then amplified and converted into a
differential voltage signal. The differential voltage
signal is then converted into a digital format.